

Module timetable - Real-Time Digital Signal Processing, MMI123176 (Wks TriBWk1-TriBWk12 (*TriBWk1-TriBWk12))

	09:00	10:00	10:00	11:00	11:00	12:00	12:00	13:00	13:00	14:00	14:00	15:00	15:00	16:00	16:00	17:00	17:00	18:00	18:00	19:00	19:00	20:00	20:00	21:00	21:00	22:00
Monday											Lec_Campus, Wks TriBWk1-TriBWk6 [=6] Module: Real-Time Digital Signal Processing Group: 16ELEENG_6_F09G1_B_MMI123176; Room: M401 Classroom			Tut_Campus, Wks TriBWk1-TriBWk6 [=6] Module: Real-Time Digital Signal Processing; Group: 16ELEENG_6_F09G1_B_MMI123176; Room: M401 Classroom												
Tue																										
We																										
Thu	Prac_Campus, Wks TriBWk1-TriBWk6 [=6] Module: Real-Time Digital Signal Processing Group: 16ELEENG_6_F09G1_B_MMI123176; Room: M326 Analogue Electronics/Electrical Power																									
Fri																										