

Module timetable - Digital Design and Computer Architecture, MHH623542 (Wks TriAWk1-TriAWk12 (\*TriAWk1-TriAWk12))

	09:00	10:00	10:00	11:00	11:00	12:00	12:00	13:00	13:00	14:00	14:00	15:00	15:00	16:00	16:00	17:00	17:00	18:00	18:00	19:00	19:00	20:00	20:00	21:00	21:00	22:00				
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Tuesday	Lec_Campus, *TriAWk1-TriAWk12 [=12] Module: Digital Design and Computer Architecture <sup>Groups</sup> ups: 13ELEENG_4_F09G1_A_ MHH623542; 13ELEENG_5_ F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_ MHH623542; 16ELEENG_5_ F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_ MHH623542; MHH623542_A_ GLAS-A_R; <b>Room: W614</b>			*TriAWk1-TriAWk12 [=12] Module: Digital Design and Computer Architecture																										
				Tut_Campus, *TriAWk1- TriAWk12 [=12] Module: Digital Design and Computer Architecture Groups: 13ELEENG_4_ F09G1_A_ MHH623542; 13ELEENG_5_ F09G1_A_ MHH623542; 16ELEENG_4_ F09G1_A_ MHH623542; 16ELEENG_5_ F09G1_A_ MHH623542; 36EXGSCE_1_ F09G1_A_ MHH623542; MHH623542_A_ GLAS-A_R; <b>Ro                      om: W614</b>																										
We																														

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Thursday					Prac_Campus, *TriAWk1-TriAWk12 [=12] Module: Digital Design and Computer Architecture, Groups: 13ELEENG_4_F09G1_A_MHH623542; 13ELEENG_5_F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_MHH623542; 16ELEENG_5_F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_MHH623542; MHH623542_A_GLAS-A_R; Room: M330_Wireless & Embedded							DirStudy_Campus, *TriAWk1-TriAWk12 [=12] Module: Digital Design and Computer Architecture, Groups: 13ELEENG_4_F09G1_A_MHH623542; 13ELEENG_5_F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_MHH623542; 16ELEENG_5_F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_MHH623542; MHH623542_A_GLAS-A_R; Room: M330_Wireless & Embedded														
Fri																										