

	09:00	10:00	10:00	11:00	11:00	12:00	12:00	13:00	13:00	14:00	14:00	15:00	15:00	16:00	16:00	17:00	17:00	18:00	18:00	19:00	19:00	20:00	20:00	21:00	21:00	22:00	
Monday			Lec_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Signal Processing Groups: 11ADTECH_4_F09G1_A_MHH623541; 13ELEENG_4_F09G1; 13ELEENG_5_F09G1 16ELEENG_4_F09G1 16ELEENG_5_F09G1; Room: M538				Tut_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Signal Processing Groups: 11ADTECH_4_F09G1_A_MHH623541; 13ELEENG_4_F09G1; 13ELEENG_5_F09G1 16ELEENG_4_F09G1 16ELEENG_5_F09G1; Room: M538				Prac_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Signal Processing Groups: 11ADTECH_4_F09G1_A_MHH623541; 13ELEENG_4_F09G1_A_MHH113285; 13ELEENG_5_F09G1_A_MHH113285; 16ELEENG_4_F09G1_A_MHH113285 16ELEENG_5_F09G1_A_MHH113285; Room: M327_Digital Signal Processing																
Tuesday			Lec_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Design and Computer Architecture Groups: 13ELEENG_4_F09G1_A_MHH623542; 13ELEENG_5_F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_MHH623542; 16ELEENG_5_F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_MHH623542; MHH623542_A_GLAS-A_R; Room: W614				Tut_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Design and Computer Architecture Groups: 13ELEENG_4_F09G1_A_MHH623542; 13ELEENG_5_F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_MHH623542; 16ELEENG_5_F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_MHH623542; MHH623542_A_GLAS-A_R; Room: W614				Prac_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Signal Processing Groups: 13ELEENG_4_F09G1_A_MHH623542; 13ELEENG_5_F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_MHH623542; 16ELEENG_5_F09G1_A_MHH623542; MHH623541_A_GLAS-A_R; Room: M327_Digital Signal Processing																
We																											

	09:00	10:00	10:00	11:00	11:00	12:00	12:00	13:00	13:00	14:00	14:00	15:00	15:00	16:00	16:00	17:00	17:00	18:00	18:00	19:00	19:00	20:00	20:00	21:00	21:00	22:00				
Thursday	<p>DirStudy_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Computer Aided Engineering Gr oups: 13ELEENG_4_F09G1_A_MHH113285; 13ELEENG_5_F09G1_A_MHH113285; 13MCHENG 3_F09G1_A_MHH113285; 16ELEENG_4_F09G1_A_ MHH113285; 16ELEENG_5_F09G1_A_MHH113285 16MCHENG_3_F09G1_A_MHH113285; 36EXGSCE 1_F09G1_A_MHH113285_Group_01; MHH113285_ A_GLAS-A_R_Group_01; Rooms: M220_CAD/ CAM Centre 2; M223_CAD/CAM Centre 1</p>												<p>Prac_Campus, Wks TriAWk9-TriAWk12 [=4] Modules: Computer Aided Engineering COMPUTER AIDED ENGINEERING Gro ups: 13EDSMGA_4_F09G1_A_MHH124813; 13ELEENG_4_F09G1_A_MHH113285; 13ELEENG_ 5_F09G1_A_MHH113285; 13ENGDMF_4_F09G1_A_ MHH124813; 13MCHENG_3_F09G1_A_MHH113285 Group_01; 13MSYENG_6_P09G1; 16ELEENG_4_ F09G1_A_MHH113285; 16ELEENG_5_F09G1_A_ MHH113285; 36EXGSCE_1_F09G1_A_MHH113285 Group_01; MHH113285_A_GLAS-A_R_Group_01 Rooms: M220_CAD/CAM Centre 2; M223_ CAD/CAM Centre 1</p>																	
							<p>Prac_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Design and Computer Architecture Gro ups: 13ELEENG_4_F09G1_A_ MHH623542; 13ELEENG_5_ F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_ MHH623542; 16ELEENG_5_ F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_ MHH623542; MHH623542_A_ GLAS-A_R; Room: M330_ Wireless & Embedded</p>												<p>DirStudy_Campus, Wks TriAWk9-TriAWk12 [=4] Module: Digital Design and Computer Architecture Gro ups: 13ELEENG_4_F09G1_A_ MHH623542; 13ELEENG_5_ F09G1_A_MHH623542; 16ELEENG_4_F09G1_A_ MHH623542; 16ELEENG_5_ F09G1_A_MHH623542; 36EXGSCE_1_F09G1_A_ MHH623542; MHH623542_A_ GLAS-A_R; Room: M330_ Wireless & Embedded</p>											
Fri																														