

Group timetable - 16ELEENG\_3\_F09G1, MEng Electrical and Electronic Engineering (Wk TriAWk12)

	09:00	10:00	10:00	11:00	11:00	12:00	12:00	13:00	13:00	14:00	14:00	15:00	15:00	16:00	16:00	17:00	17:00	18:00	18:00	19:00	19:00	20:00	20:00	21:00	21:00	22:00								
Monday							<p>Lec_Campus, Wk TriAWk12</p> <p><b>Module: Signals and Electronic System Design</b></p> <p>Groups: 11ADTECH_3_F09G1_A_M3H620587; 13ELEENG_3_F09G1_A_M3H620587; 16ELEENG_3_F09G1_A_M3H620587; 36EXGSCE_1_F09G1_A_M3H620587; M3H620587_A_GLAS-A_R; M3H620587_A_GLAS-A_R_Group_01; <b>Room: W402</b></p>				<p>Tut_Campus, Wk TriAWk12</p> <p><b>Module: Control Engineering</b></p> <p>3; Groups: 13ELEENG_3_F09G1_A_M3H627229; 13ENGDMF_3_F09G1_A_M3H627229; 13MSYENG_3_F09G1_A_M3H627229; 13EPWENG_3_F09G1_A_M3H627229; 13MCHENG_3_F09G1_A_M3H627229; 13MCHENG_5_P09G1_A_M3H627229; 13MCHENG_5_P09G1_A_M3H627229; 13MSYENG_3_F09G1_A_M3H627229; 16ELEENG_3_F09G1_A_M3H627229; 16EPWENG_3_F09G1_A_M3H627229; 16MCHENG_3_F09G1_A_M3H627229; 36EXGSCE_1_F09G1_A_M3H627229; M3H627229_A_GLAS-A_R; M3H627229_A_GLAS-A_R_Group_01; M3H627229_A_GLAS-A_R_Group_02; M3H627229_A_GLAS-A_R_Group_03; <b>Room: M323</b></p>				<p>Lec_Campus, Wk TriAWk12</p> <p><b>Module: Control Engineering 3</b></p> <p>Groups: 13ELEENG_3_F09G1_A_M3H627229; 13ENGDMF_3_F09G1_A_M3H627229; 13EPWENG_3_F09G1_A_M3H627229; 13MCHENG_3_F09G1_A_M3H627229; 13MCHENG_5_P09G1_A_M3H627229; 13MSYENG_3_F09G1_A_M3H627229; 16ELEENG_3_F09G1_A_M3H627229; 16EPWENG_3_F09G1_A_M3H627229; 16MCHENG_3_F09G1_A_M3H627229; 36EXGSCE_1_F09G1_A_M3H627229; M3H627229_A_GLAS-A_R; M3H627229_A_GLAS-A_R_Group_01; M3H627229_A_GLAS-A_R_Group_02; M3H627229_A_GLAS-A_R_Group_03; <b>Room: M323</b></p>				<p>Tut_Campus, Wk TriAWk12</p> <p><b>Module: Control Engineering 3</b></p> <p>Groups: 13ELEENG_3_F09G1_A_M3H627229; 13ENGDMF_3_F09G1_A_M3H627229; 13EPWENG_3_F09G1_A_M3H627229; 13MCHENG_3_F09G1_A_M3H627229; 13MCHENG_5_P09G1_A_M3H627229; 13MSYENG_3_F09G1_A_M3H627229; 16ELEENG_3_F09G1_A_M3H627229; 16EPWENG_3_F09G1_A_M3H627229; 16MCHENG_3_F09G1_A_M3H627229; 36EXGSCE_1_F09G1_A_M3H627229; M3H627229_A_GLAS-A_R; M3H627229_A_GLAS-A_R_Group_01; M3H627229_A_GLAS-A_R_Group_02; M3H627229_A_GLAS-A_R_Group_03; <b>Room: M323</b></p> <p><b>Electronics/ Telecommunications; M326</b></p> <p><b>Analogue Electronics/ Electrical Power</b></p>															

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	09:00	10:00	10:00	11:00	11:00	12:00	12:00	13:00	13:00	14:00	14:00	15:00	15:00	16:00	16:00	17:00	17:00	18:00	18:00	19:00	19:00	20:00	20:00	21:00	21:00	22:00
Tuesday	Lec_Campus, Wk TriAWk12 <b>Module: Communications Engineering:</b> Groups: 13ELEENG_3_F09G1; 16ELEENG_3_F09G1; M3H623517_A_GLAS-A_R; M3H623517_A_R; <b>Room: M228</b>		Tut_Campus, Wk TriAWk12 <b>Module: Communications Engineering</b> Groups: 13ELEENG_3_F09G1; 16ELEENG_3_F09G1; M3H623517_A_GLAS-A_R; M3H623517_A_R; <b>Room: M228</b>				Lec_Campus, Wk TriAWk12 <b>Module: Digital and Programmable Systems 2</b> Groups: 13ELEENG_3_F09G1; 16ELEENG_3_F09G1; 36EXGSCE_1_F09G1_A_M3H623544; M3H623544_A_R; M3H623544_A_R_Group_01; <b>Room: M319</b>		Tut_Campus, Wk TriAWk12 <b>Module: Digital and Programmable Systems 2:</b> Groups: 13ELEENG_3_F09G1; 16ELEENG_3_F09G1; 36EXGSCE_1_F09G1_A_M3H623544; M3H623544_A_R; M3H623544_A_R_Group_01; <b>Room: M319</b>																	
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Thursday	Prac_Campus, Wk TriAWk12 <b>Module: Digital and Programmable Systems 2</b> Groups: 13ELEENG_3_F09G1_A_M3H620587; 13ELEENG_3_F09G1_A_M3H627229_Group_02; 16ELEENG_3_F09G1_A_M3H620587; M3H623544_A_R_Group_01; <b>Room: M328</b> <b>Microprocessor Apps</b>		Prac_Campus, Wk TriAWk12 <b>Module: Communications Engineering:</b> Groups: 13ELEENG_3_F09G1_A_M3H620587; 13ELEENG_3_F09G1_A_M3H627229_Group_02; 16ELEENG_3_F09G1_A_M3H620587; M3H623517_A_R; <b>Room: M327</b> <b>Digital Signal Processing</b>						Tut_Campus, Wk TriAWk12 <b>Module: Signals and Electronic System Design:</b> Groups: 13ELEENG_3_F09G1_A_M3H620587; 16ELEENG_3_F09G1_A_M3H620587; 36EXGSCE_1_F09G1_A_M3H620587_Group_01; M3H620587_A_GLAS-A_R_Group_01; <b>Room: CEE_15</b>		Prac_Campus, Wk TriAWk12 <b>Module: Signals and Electronic System Design</b> Groups: 13ELEENG_3_F09G1_A_M3H620587; 16ELEENG_3_F09G1_A_M3H620587_Group_01; M3H620587_A_GLAS-A_R_Group_01; <b>Room: M326</b> <b>Analogue Electronics/Electrical Power</b>															
Friday	Prac_Campus, Wk TriAWk12 <b>Module: Digital and Programmable Systems 2</b> Groups: 13ELEENG_3_F09G1_A_M3H627229_Group_01; 16ELEENG_3_F09G1_A_M3H627229; 36EXGSCE_1_F09G1_A_M3H623544; M3H623544_A_R; <b>Room: M328</b> <b>Microprocessor Apps</b>		Prac_Campus, Wk TriAWk12 <b>Module: Communications Engineering:</b> Groups: 13ELEENG_3_F09G1_A_M3H627229_Group_01; 16ELEENG_3_F09G1_A_M3H627229; M3H623517_A_GLAS-A_R; <b>Room: M327</b> <b>Digital Signal Processing</b>						Prac_Campus, Wk TriAWk12 <b>Module: Control Engineering 3:</b> Groups: 13ELEENG_3_F09G1_A_M3H627229_Group_01; 13MCHENG_3_F09G1_A_M3H627229_Group_03; 16ELEENG_3_F09G1_A_M3H627229; M3H627229_A_GLAS-A_R_Group_02; <b>Rooms: M326</b> <b>Analogue Electronics/Electrical Power; M327</b> <b>Digital Signal Processing</b>																	